

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Yu ✓  
 Title: A PROCESS FOR  
 MANUFACTURING  
 TRANSISTORS HAVING  
 SILICON/GERMANIUM CHANNEL  
 REGIONS  
 Appl. No.: 09/599,141 ✓  
 Filing Date: 6/22/2000 ✓  
 Examiner: unknown  
 Art Unit: 2811 ✓



CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below.	
Kristine Lancelle	(Printed Name)
Kristine Lancelle	(Signature)
9/22/00	(Date of Deposit)

H. Coe  
 10-12-00  
 #2 IDS

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
 Box  
 Washington, D.C. 20231

**RECEIVED**  
 OCT - 4 2000  
 TECHNOLOGY CENTER 2800

Sir:

Pursuant to 37 C.F.R. §§ 1.97-98 and in accordance with the duty of disclosure under 37 C.F.R. §1.56, Applicant submits herewith the references listed on the attached Form PTO-1449 to the Examiner such that they may be considered and made of record in the above-identified application. A copy of each reference is enclosed.

Respectfully submitted,

Date 9-22-00

By Joseph N. Ziebert

FOLEY & LARDNER  
 Firstar Center  
 777 East Wisconsin Avenue  
 Milwaukee, Wisconsin 53202-5367  
 Telephone: (414) 297-5768  
 Facsimile: (414) 297-4900

Joseph N. Ziebert  
 Attorney for Applicant  
 Registration No. 35,421